

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

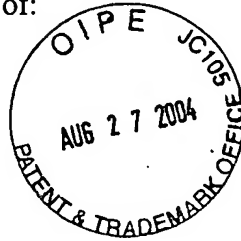
In Re PATENT APPLICATION of:

Richard B. BROWN

Appln. No.: 10/644,211

Filed: August 20, 2004

For: METHOD OF REDUCING LEAKAGE  
CURRENT IN SUB ONE VOLT SOI  
CIRCUITS



Art Unit: 2811

Examiner: Unknown

**SUBMISSION OF FORMAL DRAWINGS**

Mail Stop **Missing Parts**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed are sixteen (16) sheets of formal drawings for filing in the above-identified application.

August 25, 2004  
(Date)

Respectfully Submitted,

Charles W. Peterson, Jr.  
Registration No. 34,406

I hereby certify that this Correspondence is being deposited with the United States Postal service with sufficient postage for first class mail in an envelope address to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on:  
Date of Deposit: August 25, 2004

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